

## PCA1550A PHASE LOCKED LOOP Rev A1

Frequency Range: 1500 - 1600 MHz     • Frequency Range: 1500 - 1600 MHz     • Step Size: 1000 KHz     • cPLL - Style Package     APPLICATIONS     • Fixed Wireless Broadband Equipment     • Point-to-Point Microwave Radio     • Satellite Communication Systems	PHASE NOISE (1 Hz E	
PERFORMANCE SPECIFICATIONS	VALUE	UNITS
Frequency Range	1500 - 1600	MHz
Phase Noise @ 10 kHz offset (1 Hz BW, typ.)	-103	dBc/Hz
Harmonic Suppression (2nd, typ.)	-20	dBc
Sideband Spurs (typ.) Power Output	-70	dBc
	<u> </u>	dBm
Load Impedance Step Size	1000	Ω
Charge Pump Output Current	1250	KHz
Switching Speed (typ., adjacent channel)	2	μA mSec
Startup Lock Time (typ.)	3	mSec
Operating Temperature Range	-40 to 85	°C
Package Style	cPLL	
POWER SUPPLY REQUIREMENTS	···	
Supply Voltage (Vcc, nom.)	5	Vdc
Supply Current (Icc, typ.)	37	mA
All specifications are typical unless otherwise noted and subject to change without notice.		
APPLICATION NOTES		
AN-107 : How to Solder Z-COMM VCOs / PLLs AN-200 : Mounting and Grounding of Z-COMM PLLs		

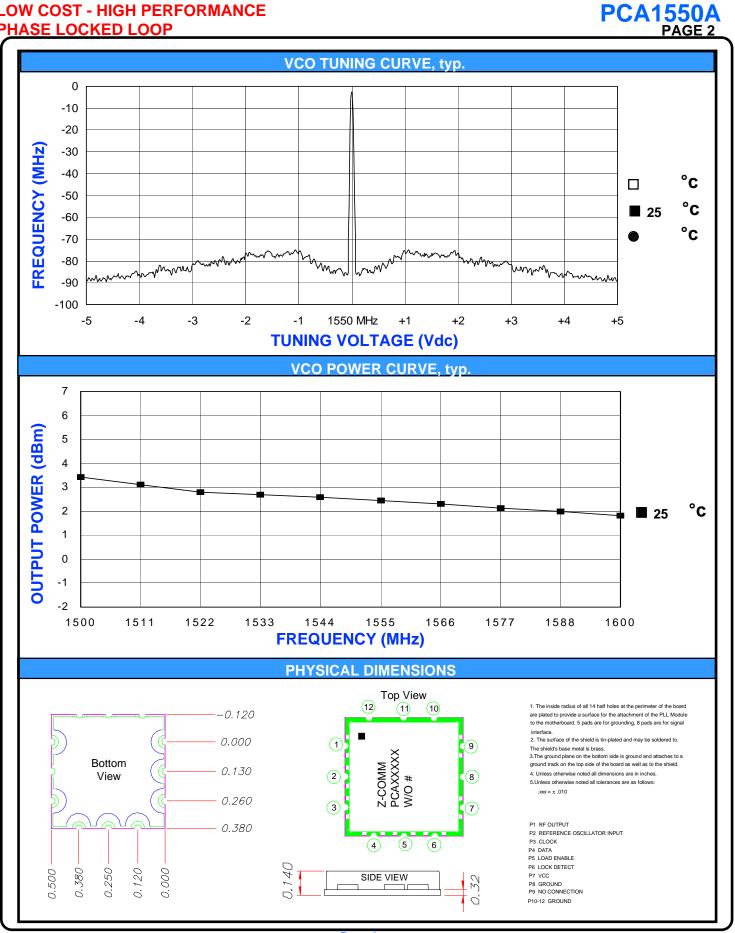
AN-200 : Mounting and Grounding of Z-COMM PLLs

• AN-201 : PLL Fundamentals AN-202 : PLL Functional Description

## NOTES:

Reference Oscillator Signal: 5 MHz<f<sub>osc</sub><100 MHz Frequency Synthesizer: Analog Devices - ADF4113

## **LOW COST - HIGH PERFORMANCE** PHASE LOCKED LOOP



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